QSFP+ LR4 40G 10km (SMF), LC duplex

SNR-QSFP+LR4-10

Single-Mode 40GBASE-LR4

QSFP+ Transceiver RoHS6 Compliant



Features

- ◆ Compliant to the IEEE 802.3ba(40GBASE-LR4)
- ◆ Compliant to the QSFP+ MSA SFF-8436 Specification
- ◆ Up to 10km over SMF
- DFBs and PIN monitor photodiodes array for transmitter section
- ◆ PIN detectors and TIAs array for receiver section
- ◆ Four 10Gbps CWDM channels in the 1300nm band
- MDIO interface with integrated Digital Diagnostic Monitoring
- ◆ Utilizes two standard LC optical connector
- ◆ Operating Case Temperature: -20°C~+65°C

Applications

- ◆ 40GBASE-LR4 Ethernet links
- Infiniband QDR and DDR interconnects Client-side
- ◆ 40G Telecom connections

Ordering Information

Part No.	Data Rate	Fiber	Distance*(note2)	Interfa ce	Temp.	DDMI
SNR-QSFP+LR4-10*(note1)	40Gbps	SMF	10km	LC	-20°C~+65°C	Yes

Note1: Standard version Note2: Over SMF

Regulatory Compliance

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Feature	Standard	Performance
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883G Method 3015.7	Class 1C (>1000 V)
Electrostatic Discharge to the enclosure	EN 55024:1998+A1+A2 IEC-61000-4-2 GR-1089-CORE	Compliant with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022:2006 CISPR 22B :2006 VCCI Class B	Compliant with standards Noise frequency range: 30MHz to 6GHz. Good system EMI design practice required to achieve Class B margins. System margins are dependent on customer host board and chassis design.
Immunity	EN 55024:1998+A1+A2 IEC 61000-4-3	Compliant with standards. 1KHz sine-wave, 80% AM, from 80MHz to 1GHz. No effect on transmitter/receiver performance is detectable between these limits.
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN (IEC) 60825-1:2007 EN (IEC) 60825-2:2004+A1	CDRH compliant and Class I laser product. ТьV Certificate No. 50135086
Component Recognition	UL and CUL EN60950-1:2006	UL file E317337 ТьV Certificate No. 50135086 (CB scheme)
RoHS6	2002/95/EC 4.1&4.2 2005/747/EC 5&7&13	Compliant with standards*note3

Note3: For update of the equipments and strict control of raw materials, SNR has the ability to supply the customized products since Jan 1, 2007, which meet the requirements of RoHS6 (Restrictions on use of certain Hazardous Substances) of European Union.

In light of item 5 in RoHS exemption list of RoHS Directive 2002/95/EC, Item 5: Lead in glass of cathode ray tubes, electronic components and fluorescent tubes.

In light of item 13 in RoHS exemption list of RoHS Directive 2005/747/EC, Item13: Lead and cadmium in optical and filter glass. The three exemptions are being concerned for SNR's transceivers, because 's transceivers use glass, which may contain Pb, for components such as lenses, windows, isolators, and other electronic components.

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Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	Ts	-40	+85	°C
Supply Voltage	Vcc	-0.5	3.6	V
Operating Relative Humidity	RH	5	85	%

^{*}Exceeding any one of these values may destroy the device immediately.

Recommended Operating Conditions

Parameter	Symbol		Min.	Typical	Max.	Unit
Operating Case Temperature	T _A SNR-QSFP+LR4-10		-5		+70	°C
Power Supply Voltage	Vcc		3.15	3.3	3.45	V
Power Supply Current	Icc				1	mA
Aggregate Bit Rate	BR _{AVE}			41.25		Gbps
Lane Bit Rate	BR _{LANE}			10.3125		Gbps

Performance Specifications - Electrical

Parar	neter	Symbol	Min.	Тур.	Max	Unit	Notes	
Transmitter								
Single ended input voltage tolerance			-0.3		4	V	Referred to TP1 signal common	
AC comm input voltag			15			mV	RMS	
	ential)	Zin	85	100	115	ohms	Rin > 100 kohms @ DC	
TX Disable	Disable Enable	V _{IH}	0		Vcc+0.3 0.8	V		
TX FAULT	Fault	V _{OH}	2.4		Vcc+0.3	V		
	Normal	V _{OL}	0		0.5			
		1	Receiv	er				
	Single ended output voltage		-0.3		4	V	Referred to signal common	
	AC common mode voltage				7.5	mV	RMS	
Termination mismatch at 1MHz					5	%		
Output Impedance (Differential)		Zout	85	100	115	ohms		
	Output Rise/Fall Time		30			ps	10%~90%	
RX_LOS _	LOS	V _{OH}	2.4		Vcc+0.3	V		
Normal		V _{OL}	0		0.8	V		

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Optical and Electrical Characteristics

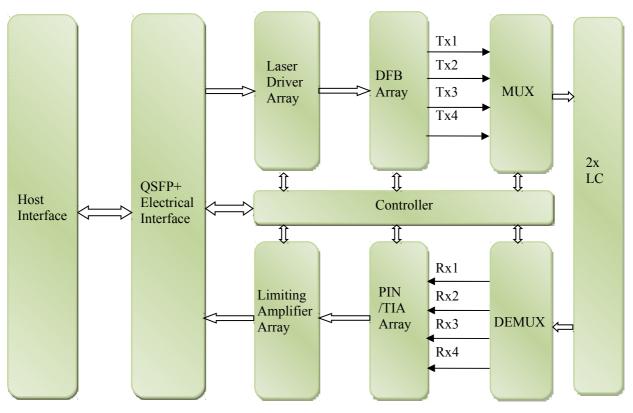
Parameter	Symbol	Min.	Typical	Max.	Unit
SMF	L	-	10	-	km
Aggregate Bit Rate	BR _{AVE}	-	40	-	Gbps
Per Lane Bit Rate	BR _{LANE}	-	10.3125	-	Gbps
T	ransmitter				
Channels wavelength	$\lambda_{ extsf{C}}$	-	1271	-	nm
		-	1291	-	
		-	1311	-	
		-	1331	-	
-20dB spectral width	Δλ	-	-	1	nm
Average Launch Power, Each Lane*(note3)	Pout/lane	-4	-	2	dBm
Transmit OMA, per Lane	TX_OMA/lane	-4	-	3.5	dBm
Extinction Ratio	Er	3	3.5	-	dB
Output Optical Eye*(note4)	IEEE 80	02.3ba-2	2010 Comp	liant	
	Receiver				
Channels wavelength	$\lambda_{ extsf{C}}$	-	1271	-	nm
		-	1291	-	
		-	1311	-	
		-	1331	-	
Damage Threshold		3	-	-	dB
Stressed receiver sensitivity in OMA, each lane	Pmins	-11.5	-	-13	dBm
Maximum Receive Power, each lane	Pmax	-	-	2.4	dBm
Receiver reflectance	Rr	-	-	-12	dB

Note3: Output is coupled into a 9/125µm Single-Mode fiber.

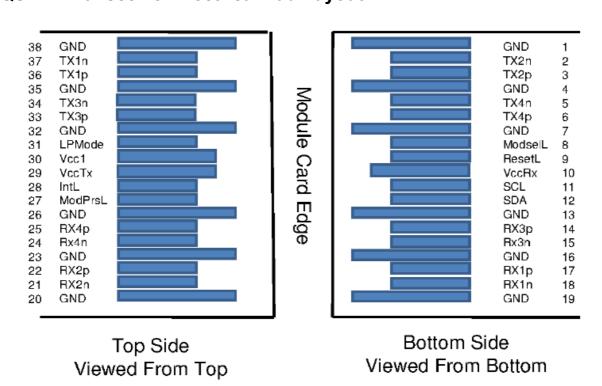
Note4: Filtered, measured with a PRBS 231-1 test pattern @10.3125Gbps

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Functional Description of Transceiver



QSFP+ Transceiver Electrical Pad Layout



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Pin Arrangement and Definition

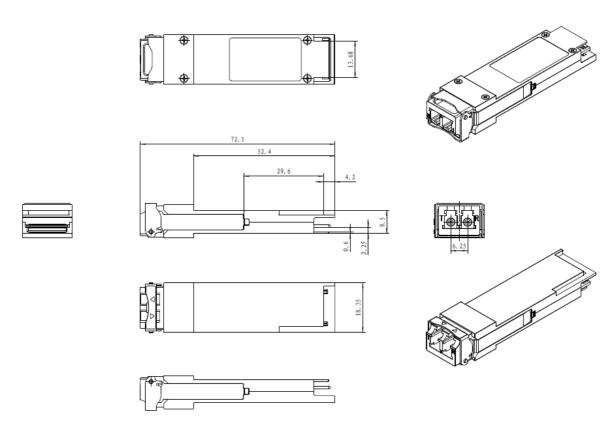
Pin	Logic	Symbol	Description	Plug Sequence	Notes
1		GND	Ground	1	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	3	
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input	3	
4		GND	Ground	1	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	3	
6	CML-I	Тх4р	Transmitter Non-Inverted Data Input	3	
7		GND	Ground	1	1
8	LVTTL-I	ModSelL	Module Select	3	
9	LVTTL-I	ResetL	Module Reset	3	
10		VccRx	+3.3V Power Supply Receiver	2	2
11	LVCMOS- I/O	SCL	2-wire serial interface clock	3	
12	LVCMOS- I/O	SDA	2-wire serial interface data	3	
13		GND	Ground	1	1
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3	
15	CML-O	Rx3n	Receiver Inverted Data Output	3	
16		GND	Ground	1	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3	
18	CML-O	Rx1n	Receiver Inverted Data Output	3	
19		GND	Ground	1	1
20		GND	Ground	1	1
21	CML-O	Rx2n	Receiver Inverted Data Output	3	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	3	
23		GND	Ground	1	1
24	CML-O	Rx4n	Receiver Inverted Data Output	3	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	3	
26		GND	Ground	1	1
27	LVTTL-O	ModPrsL	Module Present	3	
28	LVTTL-O	IntL	Interrupt	3	
29		VccTx	+3.3V Power supply transmitter	2	2
30		Vcc1	+3.3V Power supply	2	2
31	LVTTL-I	LPMode	Low Power Mode	3	
32		GND	Ground	1	1
33	CML-I	Тх3р	Transmitter Non-Inverted Data Input	3	
34	CML-I	Tx3n	Transmitter Inverted Data Input	3	
35		GND	Ground	1	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3	
37	CML-I	Tx1n	Transmitter Inverted Data Input	3	
38		GND	Ground	1	1
1: GN	ND is the symbol	for signal and	supply (power) common for the QSFF	P+ module. All ar	е

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common within the QSFP+ module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal-common ground plane.

2: Vcc Rx, Vcc1 and Vcc Tx are the receiver and transmitter power supplies and shall be applied concurrently. Requirements defined for the host side of the Host Edge Card Connector are listed in Table 6. Recommended host board power supply filtering is shown in Figures 3 and 4. Vcc Rx Vcc1 and Vcc Tx may be internally connected within the QSFP+ Module in any combination. The connector pins are each rated for a maximum current of 500mA.

Mechanical Specifications



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Notice:

SNR reserves the right to make changes or discontinue any optical link product or service identified in this publication, without notice, in order to improve design and/or performance. Applications that are described herein for any of the optical link products are for illustrative purposes only. SNR makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

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GUARANTEE:



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